

Applicant(s)	Mo-Ching Justine Lau et al
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Examiner Name	Dipakkumar Gandhi
Group Art Unit	2133
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Title: BIT ERROR RATE TESTER	

**INFORMATION
DISCLOSURE STATEMENT**

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Commissioner for Patents
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In compliance with 37 C.F.R. § 1.56, and in accordance with 37 CFR § 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement (IDS) be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP §609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Respectfully submitted,

Date:

3/7/2005

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